

Refine Search

Search Results -

Term	Documents
BOTH	772973
BOTHS	2
TOGETHER	531302
TOGETHERS	9
"SAME"	850117
SAMES	88
TWO	861200
TWOES	0
TWOS	649
TWOE	0
CONTROL	615100
(((RECEIV\$3 OR ACCEPT\$3 OR INPUT\$5) NEAR6 (BOTH OR TOGETHER OR "SAME" OR TWO) NEAR5 (CONTROL NEAR4 SIGNAL\$1 OR SCALAR NEAR4 STATES\$1) AND (STOR\$6 OR LATCH\$3) AND ACF\$1 AND FLAG\$1 AND SELECT\$5 AND RESULT\$3 AND PIPELIN\$6 AND (ARITHMETIC OR ALU\$1) AND OPERAND\$1 AND REGISTER\$1 NEAR4 (FILE\$1 OR BANK\$1 OR SET\$1 OR GROUP\$4) AND SCALAR AND (FEED\$4 OR FED) AND (PREVIOUS\$2 OR RECENT\$2 OR PRECEED\$3)).CLM.).PGPB.	0

There are more results than shown above. Click here to view the entire set.

Database:

US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database

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IBM Technical Disclosure Bulletins

Search:

L42

Refine Search

Recall Text



Clear

Interrupt

Search History

DATE: Thursday, November 17, 2005 [Printable Copy](#) [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
	<i>DB=PGPB; PLUR=YES; OP=OR</i>		
<u>L42</u>	((receiv\$3 or accept\$3 or input\$5) near6 (both or together or "same" or two) near5 (control near4 signal\$1 or scalar near4 state\$1) and (stor\$6 or latch\$3) and ACF\$1 and flag\$1 and select\$5 and result\$3 and pipelin\$6 and (arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	0	<u>L42</u>
<u>L41</u>	((receiv\$3 or accept\$3 or input\$5) near8 (both or together or "same" or two) near5 (condition\$3 or signal\$1 or state\$1) and (stor\$6 or latch\$3) and ACF\$1 and flag\$1 and select\$5 and result\$3 and pipelin\$6 and (arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	4	<u>L41</u>
<u>L40</u>	((receiv\$3 or accept\$3 or input\$5) near8 (condition\$3 or signal\$1 or state\$1) and (stor\$6 or latch\$3) and ACF\$1 and flag\$1 and select\$5 and result\$3 and pipelin\$6 and (arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	4	<u>L40</u>
<u>L39</u>	((receiv\$3 or accept\$3 or input\$5) near12 (condition\$3 or signal\$1 or state\$1) and (stor\$6 or latch\$3) and ACF\$1 and flag\$1 and select\$5 and result\$3 and pipelin\$6 and (arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	4	<u>L39</u>
<u>L38</u>	((stor\$6 or latch\$3) and ACF\$1 and flag\$1 and select\$5 and result\$3 and pipelin\$6 and (arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	4	<u>L38</u>
<u>L37</u>	(ACF\$1 and flag\$1 and select\$5 and result\$3 and pipelin\$6 and (arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	4	<u>L37</u>
<u>L36</u>	(ACF\$1 and flag\$1 and select\$5 and result\$3 and pipelin\$6 and (arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	0	<u>L36</u>
<u>L35</u>	(select\$5 and result\$3 and pipelin\$6 and (arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar near45 (both or together or "same" or two) and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	0	<u>L35</u>
<u>L34</u>	(select\$5 and result\$3 and pipelin\$6 and (arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar near15 (both or together or "same" or two) and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	0	<u>L34</u>

<u>L33</u>	(select\$5 and result\$3 and pipelin\$6 and (arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	4	<u>L33</u>
<u>L32</u>	L31 and select\$5	4	<u>L32</u>
<u>L31</u>	L30 and result\$3	4	<u>L31</u>
<u>L30</u>	((arithmetic or alu\$1) and operand\$1 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4) and scalar and (feed\$4 or fed) and (previous\$2 or recent\$2 or preceed\$3)).clm.	4	<u>L30</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L29</u>	L26 and l11	7	<u>L29</u>
<u>L28</u>	L26 and l10	10	<u>L28</u>
<u>L27</u>	L26 and l9	114	<u>L27</u>
<u>L26</u>	L25 and arithmetic\$3 near6 (flag\$4 or bit\$1 or state\$1 or condition\$3)	397	<u>L26</u>
<u>L25</u>	L23 near15 (next or subsequent\$2 or previous\$2 or preceed\$3 or last\$2 or recent\$2 or current\$2)	2908	<u>L25</u>
<u>L24</u>	L23 near15 (previous\$2 or preceed\$3 or last\$2 or recent\$2 or current\$2)	1821	<u>L24</u>
<u>L23</u>	l1 near25 (Boolean or logic\$3)	19508	<u>L23</u>
<u>L22</u>	l1 and (Boolean or logic\$3)	82803	<u>L22</u>
<u>L21</u>	L18 and l11	0	<u>L21</u>
<u>L20</u>	L18 and l10	5	<u>L20</u>
<u>L19</u>	L18 and l9	16	<u>L19</u>
<u>L18</u>	scalar near45 l4	23	<u>L18</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L17</u>	l8 and l11	0	<u>L17</u>
<u>L16</u>	l8 and l10	4	<u>L16</u>
<u>L15</u>	l8 and l9	21	<u>L15</u>
<u>L14</u>	l4 and l11	6	<u>L14</u>
<u>L13</u>	l4 and l10	50	<u>L13</u>
<u>L12</u>	l4 and l9	400	<u>L12</u>
<u>L11</u>	(708/236-252)![CCLS]	571	<u>L11</u>
<u>L10</u>	(712/11-22, 23,24, 225, 230,221)[CCLS]	1210	<u>L10</u>
<u>L9</u>	(712/2-300)[CCLS]	11580	<u>L9</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L8</u>	l4 near15 operand\$1	95	<u>L8</u>
<u>L7</u>	L6 and register\$1 near4 (file\$1 or bank\$1 or set\$1 or group\$4 or block\$1)	291	<u>L7</u>
<u>L6</u>	L4 and (fed near5 back or feed\$4 near5 back or feedback)	325	<u>L6</u>
<u>L5</u>	L4 near25 (fed near5 back or feed\$4 near5 back or feedback)	13	<u>L5</u>
<u>L4</u>	L2 near15 condition\$3	1170	<u>L4</u>
<u>L3</u>	L2 and (fed near5 back or feed\$4 near5 back or feedback)	1575	<u>L3</u>
<u>L2</u>	L1 near15 (arithmetic\$3 or alu\$1)	5173	<u>L2</u>
<u>L1</u>	(accumulat\$3 or psw\$1 or register\$1) near8 (condition\$3 or status or state\$1 or zero or carry or c or overflow or o)	217299	<u>L1</u>

END OF SEARCH HISTORY